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| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
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| 09/816,967 | 03/23/2001 | Gregory J. Mann | BUR9-2001-0025-US1 | 8686 |

29154 7590 01/24/2005

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| EXAMINER |
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FAROOQ, MOHAMMAD O

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| ART UNIT | PAPER NUMBER |
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2182

DATE MAILED: 01/24/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/816,967

Applicant(s)

MANN, GREGORY J.

Examiner

Mohammad O. Farooq

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 08 October 2004.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-21 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-21 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 11 May 2001 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

1. Claims 1-21 are rejected under 35 U.S.C. 103(a) as being unpatentable over

Aguilar et al. U.S. Pat. No. 6,199,137 in view of Garland et al. 6,389,120.

2. As to claim 1, Aguilar et al. teach a core for providing communications between a transmission media and a processor in a parallel-serial architecture, said core comprising:

serial lanes connecting said processor to said transmission media (i.e. via port; see fig. 2); and

at least one selector (data MUX) connected to said serial lanes (see fig. 2).

Aguilar et al. do not teach selector selectively engages said serial lanes to alter speed of data passing through said core. Garland et al. teach selector selectively engages said serial lanes to alter speed of data passing through said core (i.e. by buffering; col. 3, line 60 – col. 4, line 29). However, it would have been obvious to one of ordinary skill in the art at the time of invention to combine the teachings of Aguilar et al. and Garland et al. because that would provide coordination for complex communication system (col. 1, lines 27-35).

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3. As to claims 2, 9, and 16 Aguilar et al. teach core further comprising a data controller (router; item 230, fig. 2) for controlling an operation of said selector.

4. As to claims 3, 10, and 17 Aguilar et al. teach wherein said serial lanes include buffers for performing additional speed alteration of said data (see fig. 2).

5. As to claims 4, 11, and 18 Aguilar et al. teach wherein said buffers comprise elastic (inherent) first-in, first-out (FIFO) buffers (see fig. 2).

6. As to claims 5, 12, and 19 Aguilar et al. teach wherein said selector comprises a multiplexor (see item 250, fig. 2).

7. As to claims 6, 13, and 20 Aguilar et al. teach wherein additional speed adjustments is attained by said selector engaging additional lanes (see fig. 2).

8. As to claims 7, 14, and 21 Aguilar et al. teach wherein said transmission media operates at a different data speed than said processor (inherent; see fig. 2).

9. As to claim 8, Aguilar et al. teach a parallel-serial system comprising:
at least one processor (item 210, fig. 2);
at least one transmission media (via ports; item 240, fig. 2) connecting said one processor (see fig. 2); and

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a core between each processor and said transmission media, said core providing communication between said transmission media and said processor, and said core comprising:

serial lanes connecting said processor to said transmission media (i.e. via port; see fig. 2); and

at least one selector (data MUX) connected to said serial lanes (see fig. 2).

Aguilar et al. do not teach selector selectively engages said serial lanes to alter speed of data passing through said core. Garland et al. teach selector selectively engages said serial lanes to alter speed of data passing through said core (i.e. by buffering; col. 3, line 60 – col. 4, line 29). However, it would have been obvious to one of ordinary skill in the art at the time of invention to combine the teachings of Aguilar et al. and Garland et al. because that would provide coordination for complex communication system (col. 1, lines 27-35).

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10. As to claim 15, Aguilar et al. teach a core for providing communications between a transmission media and a processor in a byte-stripped parallel-serial architecture, said core comprising:

serial lanes connecting said processor to said transmission media (i.e. via port; see fig. 2); and

at least one selector (data MUX) connected to said serial lanes (see fig. 2).

Aguilar et al. do not teach selector selectively engages said serial lanes to alter speed of data passing through said core. Garland et al. teach selector selectively engages said serial lanes to alter speed of data passing through said core (i.e. by buffering; col. 3, line 60 – col. 4, line 29). However, it would have been obvious to one of ordinary skill in the art at the time of invention to combine the teachings of Aguilar et al. and Garland et al. because that would provide coordination for complex communication system (col. 1, lines 27-35).

Response to Arguments

11. Applicant's arguments filed October 8, 2004 have been fully considered but they are not persuasive.

12. The examiner disagrees with the applicant's representative the references (i.e. Aguilar in view of Garland) do not teach the applicant's invention. The examiner would like to point to items 202 and 106 of figure 2 of Garland et al. where these two items could be considered as a part of the core which the applicant claims as his invention. Item 202 is a router/multiplexor and item 106 is a buffer; and these two items in combination perform the function of adjusting different data rates (see col. 4, lines 9-15). The Garland et al. reference specifically mentions "... buffer function provided by buffer 120 is included in router/multiplexor 202...buffering is used to adjust for the different data rates across the various data communications paths." Therefore, the examiner maintains the rejection.

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13. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

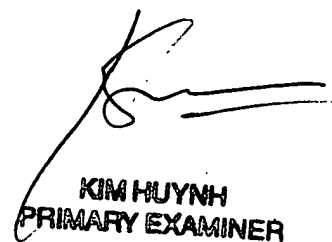
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14. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mohammad O. Farooq whose telephone number is (571) 272-4144. The examiner can normally be reached on 9:00am - 5:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jeffrey A. Gaffin can be reached on (571) 272-4146. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Mohammad O. Farooq
January 14, 2005


KIM HUYNH
PRIMARY EXAMINER

1/19/05